LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY
Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada
Accredited by NAAC with "A" Grade, NBA (CSE, ECE, EEE, ME)
Jonnada (Village), Denkada (Mandal), Vizianagaram Dist – 535 005
Phone No. 08922-241111, 241112
E-Mail: lendi_2008@yahoo.com Website: www.lendi.org

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

List of Projects
(Academic Year: 2017-18)

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
1	Analysis of DMT GDI CMOS Logic	VLSI Design	Research	PO1-PO12, PSO1,PSO2
2	Miniaturized IoT Antenna For Sub-1 GHZ Applications	Antennas	Application	PO1-PO12, PSO1,PSO2
3	Low Power Split Radix FFT Implementation Using Radix 2 Butterfly	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
4	Circularly Polarized Telemetry Conformal Antennas For Unmanned Area Vehicles	Antennas	Research	PO1-PO12, PSO1,PSO2
5	Analysis of Mobile Broad Band Communication In 5G System Using Orthogonal Codes	Communications	Research	PO1-PO12, PSO1,PSO2
6	GDI Base Area Delay Power Efficient Carry Select Adder	VLSI Design	Research	PO1-PO12, PSO1,PSO2
7	A Novel Architecture of High Speed And Area Efficient Wallace Tree Multiplier Using Carry Select Adder With Mirror Adder	VLSI Design	Research	PO1-PO12, PSO1,PSO2
8	Design of Fast Binary Counters Using Multipliers on Stacking Compressor	VLSI Design	Application	PO1-PO12, PSO1,PSO2
9	High Performance Booth Encoded Wallace Tree Multiplier Using Modified Full Adder	VLSI Design	Research	PO1-PO12, PSO1,PSO2
10	Drowsy Driver Detection And Altering System Using Embedded System	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
11	Railway Tracking Fault Monitoring System Using Wavelet Domain Operator Based On Signal Separation	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
12	Real time IRIS recognition by using Neural networks	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
13	Performance analysis Of 2*2 multiplier using hybrid full adder	VLSI Design	Research	PO1-PO12, PSO1,PSO2
14	Micro strip Band Pass Fractal Filter	Antennas	Research	PO1-PO12, PSO1,PSO2
15	Estimation of Power And Delay of CMOS Circuits Using Leakage Control Transistors	VLSI Design	Research	PO1-PO12, PSO1,PSO2
16	Smart Image Analysis Based GRI Advisory System for Rice crops	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
17	Design of Low Power Full Adders	VLSI Design	Research	PO1-PO12, PSO1,PSO2
18	A Wideband Circularly Polarized Dielectric Resonator Antenna WLAN/W1-MAZ Applications	Antennas	Application	PO1-PO12, PSO1,PSO2
19	Removal of High Density Using BPANN-Modified Median Filter Technique	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
20	Design And Implementation of Low Power High Speed Hybrid Four Bit parallel Adder Circuit	VLSI Design	Research	PO1-PO12, PSO1,PSO2
21	GSM Based Patient Diagnosis And Simulation	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
22	CP Patch Antenna With Controllable Polarization over Dual Frequency Bands	Antennas	Research	PO1-PO12, PSO1,PSO2
23	Fast multi region Image Segmentation Using Active Contour	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
24	An Energy and area efficient 4:2 compressor based on Fin FETs	VLSI Design	Research	PO1-PO12, PSO1,PSO2

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
25	Using SVD-FRFT Filtering to suppress first order sea clutter in HFSWR	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
26	Air Quality Monitoring System	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
27	Compact Micro Strip Wide Band Pass Filter Using High Selectivity	Antennas	Research	PO1-PO12, PSO1,PSO2
28	Low Power Split Radix 16-Point FFT Processor Implementation	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
29	Real Time Eye Motion Based Wheel Chair Control Using Raspberry Pi	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
30	Deep Location Specific Tracking Using Convolution Neutral Networks	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
31	Autonomous Vacuum Cleaning Robot	Embedded Systems & IoT	Product	PO1-PO12, PSO1,PSO2
32	An Efficient Bio-Metric System Using Iris Detection	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
33	Faster Acquisition for Software Defined GPS Receiver	Communications	Research	PO1-PO12, PSO1,PSO2
34	Rectangular and Hexagonal Shape Micro strip Multi Band pass Filter	Antennas	Research	PO1-PO12, PSO1,PSO2
35	Tilted Dielectric Resonator Antenna	Antennas	Research	PO1-PO12, PSO1,PSO2
36	Tumor Identification in MRI Brain Image Using Fuzzy C-Means Algorithm	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
37	Power Efficient Carry Select Adder Using Brent Kung Adder	VLSI Design	Research	PO1-PO12, PSO1,PSO2
38	Digital Image Water Marking Technique	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2
39	Reduction of Noise from ECG Signal Using Digital FIR Filter With composite windowing technique	Image & Signal Processing	Research	PO1-PO12, PSO1,PSO2

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
40	Compact MIMO Slot Antenna For Wide Band Application	Antennas	Research	PO1-PO12, PSO1,PSO2
41	Comparator Implemented In 130nm Using CMOS Domino Logic	VLSI Design	Research	PO1-PO12, PSO1,PSO2
42	Design of Based Reversible Logic Gates Using QCA	VLSI Design	Application	PO1-PO12, PSO1,PSO2
43	Dual Segment S-Shaped Aperture Coupled Cylindrical DR Antenna For X-Band Application	Antennas	Research	PO1-PO12, PSO1,PSO2
44	Reduction of Delay, Power and Area by Using Parallel Prefix Adder	VLSI Design	Application	PO1-PO12, PSO1,PSO2
45	Design of low power single phase FLIP- FLOP using 19 transistors	VLSI Design	Application	PO1-PO12, PSO1,PSO2
46	Design a Performance Analysis of 2:1 Multiplexing Using Multiple Logic Families of 130nm Technology	VLSI Design	Application	PO1-PO12, PSO1,PSO2

Project Coordinator

Head of the Department, ECE

Head of the Department
Electronics & Communication Engg.
LBRDI Institute of Engg. & Technology
Kommadi- 535005, Vizianagaram Dist